

HIGH TEMPERATURE SMPS STACKED CAPACITORS (HTS)

Consult Factory for Requirements Above 250°C

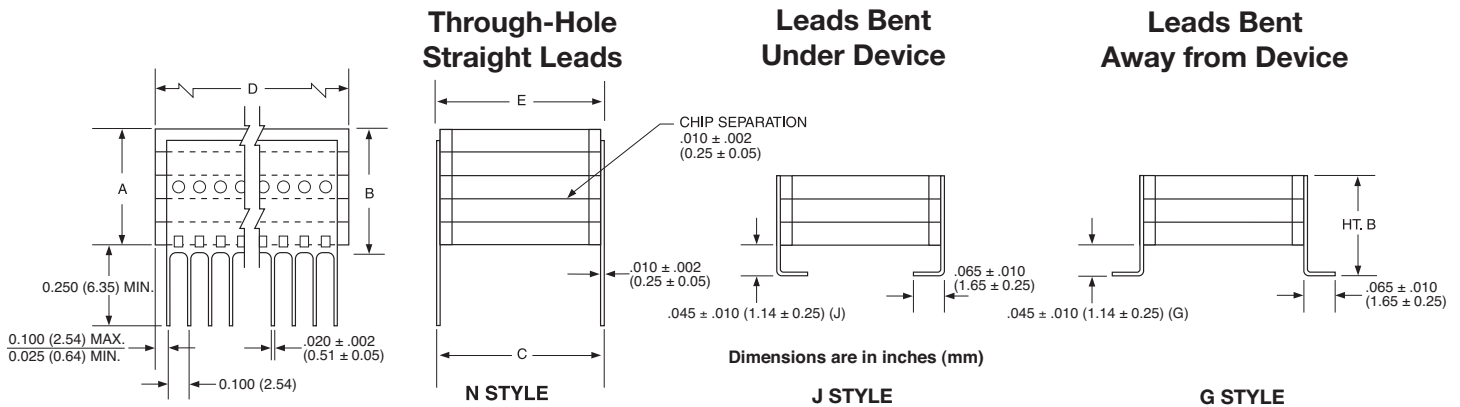
SOLDERING AND LEAD COATING RECOMMENDATIONS

Special Code	Description	Attachment Method Recommended Max. Temp.	RoHS Status
Blank	Sn63 / HMP Compatible	Sn63 or HMP (150°C / 200°C)	Not Compliant
X	Sn96 / HMP Compatible	Sn96 or HMP (180°C / 250°C)	Compliant with Exemption 7A.
Y	Sn96 / HMP Compatible	Sn96 or HMP (180°C / 250°C)	Not Compliant
R	Sn96	Sn96 (180°C) DO NOT USE HMP	Compliant (Lead Free)

NON-MAGNETIC STACKS AVAILABLE (RoHS 200°C – Non-RoHS 250°C)

SURFACE MOUNT STACKS

PRESIDIO LEAD STYLES AND DIMENSIONS



HOW TO ORDER HTS STACKS

HT	S	4	05	X7R	804	K	2	J	4	Y	(F)
PREFIX HT	CONFIGURATION STACKED Capacitor Assembly	NO. OF CAPS No. of chips per stack	CASE CODE See Table Pg. 11 **	CAPACITANCE CODE Two significant figures followed by the number of zeros. Example: 100 = 10 pF 101 = 100 pF 102 = 1000 pF 103 = .01 μF	TOLERANCE CODE F = ± 1% ≥ 10pF G = ± 2% ≥ 10pF J = ± 5% ≥ 10pF K = ± 10% L = -10% / +20% M = ± 20% Z = +80% / -20% P = +100% / -0%	VOLTAGE CODE 1 = 25 VDC 2 = 50 VDC 3 = 100 VDC A = 150 VDC 4 = 200 VDC & = 250 VDC 5 = 300 VDC 6 = 500 VDC 7 = 600 VDC 8 = 750 VDC # = 800 VDC 9 = 1000 VDC **	LEAD STYLE J = Leads formed under G = Leads formed out N = Through-hole * = Soft-leaded See Above Consult Factory for more information	NO. OF LEADS PER SIDE See Page 11	SPECIAL CODE See Above (Leave blank or add Code X, Y or R)	DESIGN-IN CODE See Page 15	
		DIELECTRIC NPQ* NPO NHT N2T* (175°C Max) X7R XHT									

* Contact factory regarding NPQ and N2T dielectric
** Contact factory for other case codes/sizes and voltages



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