

# HIGH VOLTAGE STACKED CAPACITORS

PRESIDIO COMMON SIZES - X7R AND NPO

MAXIMUM CAPACITANCE ( $\mu\text{F}$ )

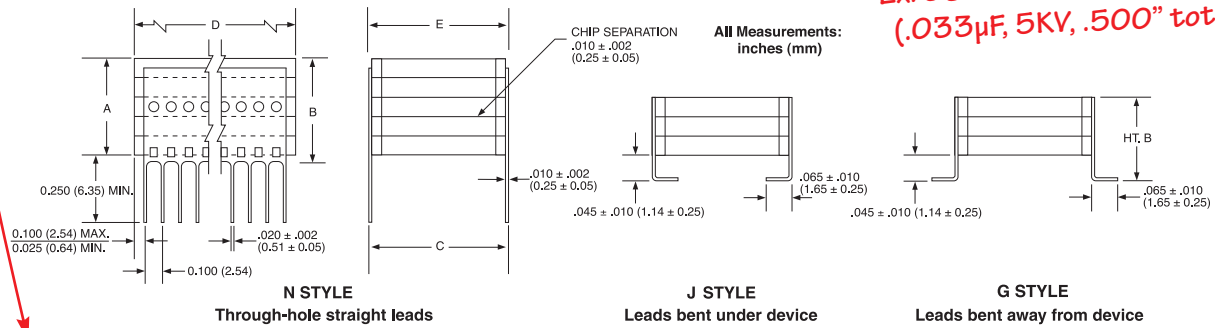
Ex: S156NPO303K9J6  
(.03 $\mu\text{F}$ , 1KV, .200" total height)

Ex: S358X7R214K13N6  
(.21 $\mu\text{F}$ , 3KV, .500" total height)

PRESIDIO CASE SIZE																"B" Ht. Max. Inch	No. of Caps per stack	
Case Code	52		53		37		54		55		56		57		58			
Dielectric	X7R	NPO	X7R	NPO	X7R	NPO	X7R	NPO	X7R	NPO	X7R	NPO	X7R	NPO	X7R	NPO		
1000V (voltage code=9)	.040	.0036	.080	.0075	.12	.010	.16	.014	.25	.022	.35	.030	.38	.033	.70	.060	.200 (5.08)	1
	.080	.0072	.16	.015	.24	.020	.32	.028	.50	.044	.70	.060	.76	.066	1.4	.12	.350 (8.89)	2
	.12	.011	.24	.022	.36	.030	.48	.042	.75	.066	1.0	.090	1.1	.10	2.1	.18	.500 (12.70)	3
2000V (voltage code=11)	.080	.00075	.019	.0017	.027	.0024	.035	.0032	.055	.0050	.080	.0070	.090	.0082	.17	.015	.200 (5.08)	1
	.16	.0015	.038	.0034	.054	.0048	.070	.0064	.11	.010	.16	.014	.18	.016	.34	.030	.350 (8.89)	2
	.24	.0022	.057	.0051	.081	.0072	.10	.0096	.16	.015	.24	.021	.27	.024	.51	.045	.500 (12.70)	3
3000V (voltage code=13)	.032	.0030	.076	.0068	.10	.0096	.14	.013	.22	.020	.32	.028	.36	.033	.68	.060	.650 (16.51)	4
	--	--	.0070	.00065	.011	.0010	.014	.0013	.022	.0021	.033	.0030	.039	.0035	.070	.0065	.200 (5.08)	1
	--	--	.014	.0013	.022	.0020	.028	.0026	.044	.0042	.066	.0060	.078	.0070	.14	.013	.350 (8.89)	2
4000V (voltage code=14)	--	--	.021	.0019	.033	.0030	.042	.0039	.066	.0063	.10	.0090	.11	.010	.21	.019	.500 (12.70)	3
	--	--	.028	.0026	.044	.0040	.056	.0052	.088	.0084	.13	.012	.15	.014	.28	.026	.650 (16.51)	4
	--	--	--	--	.0055	.00050	.007	.00060	.012	.0010	.017	.0015	.020	.0018	.039	.0035	.200 (5.08)	1
5000V (voltage code=15)	--	--	--	--	.011	.0010	.014	.0013	.022	.0021	.033	.0030	.039	.0035	.070	.0065	.200 (5.08)	1
	--	--	--	--	.0060	.00066	.0080	.00084	.013	.0014	.018	.0020	.022	.024	.044	.0048	.350 (8.89)	2
	--	--	--	--	.0090	.0010	.012	.0012	.019	.0021	.027	.0030	.033	.036	.066	.0072	.500 (12.70)	3
DIMENSIONS inches (mm)	.300 (7.62)		.415 (10.54)		.550 (13.97)		.500 (12.70)		.600 (15.24)		.700 (17.78)		.975 (24.77)		1.375 (34.93)		C $\pm$ .025 (0.64)	
	.260 (6.60)		.350 (8.89)		.320 (8.13)		.460 (11.68)		.560 (14.22)		.660 (16.76)		.520 (13.21)		.670 (17.02)		D (Max.) Width	
	.325 (8.26)		.440 (11.18)		.580 (14.73)		.525 (13.34)		.625 (15.88)		.725 (18.42)		1.000 (25.40)		1.400 (35.56)		E (Max.) Length	
Leads per Side	3		4		3		4		5		6		5		6			
Chip Size	2824		3933		5330		4844		5854		6864		9650		13565			

Note: Other sizes, capacitances, and voltage ratings are available. Consult factory.

## PRESIDIO LEAD STYLE AND DIMENSIONS



Ex: S357XR7333M15G5  
(.033 $\mu\text{F}$ , 5KV, .500" total height)

## HOW TO ORDER

—	S	3	52	X7R	124	K	9	J	3
Optional Screening Code	Configuration	No. of Caps	Case Code	Dielectric	Capacitance	Capacitance Tolerance	Voltage	Lead Style	No. of Leads
HR or SR (See pg. 7)	Stacked Capacitor Assembly	Number of Capacitors per Stack	See Above	X7R NPO	Capacitance (in picofarads): Two significant figures followed by the number of zeros. Example: 103=10,000 pF=.01 $\mu\text{F}$	F = $\pm$ 1% (NPO only) G = $\pm$ 2% (NPO only) J = $\pm$ 5% (NPO only) K = $\pm$ 10% M = $\pm$ 20% Z = -20%/+80%	9 = 1000V 11 = 2000V 13 = 3000V 14 = 4000V 15 = 5000V	J = Leads formed under G = Leads formed out N = Through-hole S = See pages 12 & 13	Number of Leads per Side (See Above)